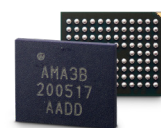
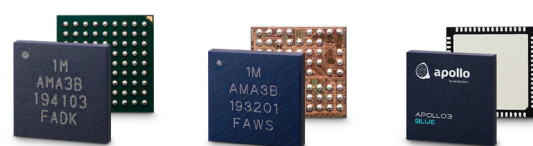


Available  
1Q2025

	Apollo2	Apollo3	Apollo4 Lite	Apollo4 Plus	Apollo510
<b>SoC Frequency</b>	48 MHz	48 MHz 96 MHz turboSPOT®	96 MHz 192 MHz turboSPOT	96 MHz 192 MHz turboSPOT	96 MHz 250 MHz turboSPOT
<b>SoC</b>	32-bit Arm Cortex-M4F	32-bit Arm Cortex-M4F, DMA	32-bit Arm Cortex-M4F, DMA	32-bit Arm Cortex-M4F, DMA	32-bit Arm Cortex-M55 with Helium, DMA
<b>SoC CoreMark® Power Efficiency</b>	39 µW/MHz	33 µW/MHz	38 µW/MHz	29 µW/MHz	Contact Ambiq
<b>NVM</b>	1MB	1MB	2MB	2MB	4MB
<b>SRAM</b>	256KB	384KB	1.4MB	2.75MB	3.75MB
<b>Voltage</b>	1.755-3.63 V	1.755-3.63 V	1.71-2.2 V	1.71-2.2 V	1.71-2.2 V
<b>ADC</b>	14-bit, 15-channel, up to 2.67 MS/s Sampling Rate ADC	14-bit, 15-channel, up to 2.67 MS/s Sampling Rate ADC	12-bit, 11-channel, up to 2.8 MS/s Sampling Rate ADC	12-bit, 11-channel, up to 2.8 MS/s Sampling Rate ADC	12-bit, 11-channel, up to 2.8 MS/s Sampling Rate ADC
<b>UART</b>	2	2	4	4	4
<b>I/O</b>	I <sup>2</sup> C/SPI Master (6x)	I <sup>2</sup> C/SPI Master (6x) I <sup>2</sup> C/SPI Slave	I <sup>2</sup> C/SPI Master (8x) I <sup>2</sup> C/SPI Slave SDIO v3.0/eMMC (1x)	I <sup>2</sup> C/SPI Master (8x) I <sup>2</sup> C/SPI Slave USB 2.0 FS/HS SDIO v3.0/eMMC (1x)	I <sup>2</sup> C/SPI Master (8x) 1x full-duplex/1x half-duplex I <sup>2</sup> C/SPI Slave USB 2.0 FS/HS SDIO v3.0/eMMC (2x) Support for multiple I/O voltages ranging 1.14 to 3.63V
<b>MSPI Master</b>	--	1/2/4/8-bit wide up to 48 MT/s (SDR) ISO7816 Master	1/2/4/8-bit wide (2x) 1/2/4/8/16-bit wide up to 96 MT/s (SDR/DDR)	1/2/4/8-bit wide (2x) 1/2/4/8/16-bit wide up to 96 MT/s (SDR/DDR)	1/2/4/8-bit wide (2x) up to 96 MT/s (SDR/DDR) 1/2/4/8/16-bit wide (2x) up to 250 MT/s (SDR/DDR)
<b>I<sup>2</sup>S</b>	I <sup>2</sup> S Slave for PDM Audio Pass-through	I <sup>2</sup> S Slave for PDM Audio Pass-through	I <sup>2</sup> S Master/Slave (2x) full-duplex	I <sup>2</sup> S Master/Slave (2x) full-duplex with ASRC (2x)	I <sup>2</sup> S Master/Slave (2x) full-duplex, with ASRC (1x)
<b>Audio</b>	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (4x) Low Power Stereo Audio ADC (1x)	PDM stereo DMIC interface (1x) Low Power Audio ADC (1x) PLL for Precision Audio
<b>Display</b>	--	SPI 3-wire/4-wire	SPI 3-wire/4-wire Dual/QuadSPI	SPI 3-wire/4-wire Dual/QuadSPI MIPI DSI at 500 Mbps (2 lanes) 4-layer Display Controller	SPI 3-wire/4-wire Dual/QuadSPI MIPI DSI at 1.5 Gbps (2 lanes) 4-layer Display Controller Memory in Pixel (MiP) Interface
<b>Graphics</b>	--	--	2D/2.5D GPU with anti-aliasing, and dithering	2D/2.5D GPU with anti-aliasing, dithering, and vector graphics assist	2D/2.5D GPU with anti-aliasing, dithering, and HW vector graphics acceleration
<b>Security</b>	--	secureSPOT®	secureSPOT 2.0 Secure Boot, Root of Trust	secureSPOT 2.0 Secure Boot, Root of Trust	secureSPOT 3.0 Arm TrustZone®
<b>Packages</b>	<ul style="list-style-type: none"> <li>4.5 x 4.5 mm, BGA-64 w/ 50 GPIO</li> <li>2.6 x 2.6 x 0.51 mm, CSP-49 w/ 34 GPIO</li> <li>2.6 x 2.6 x 0.33 mm, CSP-49 w/ 34 GPIO (Thin)</li> </ul>	<ul style="list-style-type: none"> <li>5 x 5 mm, BGA-81 w/ 50 GPIO</li> <li>3.25 x 3.37 mm, CSP-66 w/ 37 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>5 x 5 mm, BGA-146 w/ 84 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>5 x 5 mm, BGA-146 w/ 105 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>6.6 x 6.6 mm, BGA with 183 GPIO</li> <li>4.9 x 4.7 mm, CSP with 144 GPIO</li> </ul>
<b>Ordering Information</b>	<ul style="list-style-type: none"> <li>AMAPH1KK-KBR (BGA)</li> <li>AMAPH1KK-KCR (CSP)</li> <li>AMAPH1KK-KCR-TB (Thin CSP)</li> <li>AMAPHEVB (EVB)</li> </ul>	<ul style="list-style-type: none"> <li>AMAP31KK-KBR (BGA)</li> <li>AMAP31KK-KCR (CSP)</li> <li>AMA3BEVB</li> </ul>	<ul style="list-style-type: none"> <li>AMAP42KL-KBR (BGA)</li> <li>AMAP4LEVB (EVB)</li> </ul>	<ul style="list-style-type: none"> <li>AMAP42KP-KBR (BGA)</li> <li>AMAP4PEVB (EVB)</li> <li>AMAP4PDISP (Display Kit)</li> </ul>	<ul style="list-style-type: none"> <li>AP510NFA-CBR (BGA)</li> <li>AP510NFA-CCR (CSP)</li> </ul>



	Apollo3 Blue	Apollo3 Blue Plus	Apollo4 Blue Lite	Apollo4 Blue Plus
<b>SoC Frequency</b>	48 MHz, 96 MHz turboSPOT	48 MHz, 96 MHz turboSPOT	96 MHz, 192 MHz turboSPOT	96 MHz, 192 MHz turboSPOT
<b>SoC</b>	32-bit Arm Cortex-M4F, DMA, Arm Cortex-M0 for Bluetooth Low Energy	32-bit Arm Cortex-M4F, DMA, Arm Cortex-M0 for Bluetooth Low Energy	32-bit Arm Cortex-M4F, DMA, Arm Cortex-M0 for Bluetooth Low Energy	32-bit Arm Cortex-M4F, DMA, Arm Cortex-M0 for Bluetooth Low Energy
<b>SoC CoreMark Power Efficiency</b>	33 $\mu$ W/MHz	33 $\mu$ W/MHz	38 $\mu$ W/MHz	29 $\mu$ W/MHz
<b>NVM</b>	1MB	2MB	2MB	2MB
<b>SRAM</b>	384KB	768KB	1.4MB	2.75MB
<b>Voltage</b>	1.755-3.63 V	1.755-3.63 V	1.71-2.2 V	1.71-2.2 V
<b>ADC</b>	14-bit, 15-channel, up to 2.67 MS/s Sampling Rate ADC	14-bit, 15-channel, up to 2.67 MS/s Sampling Rate ADC	12-bit, 11-channel, up to 2.8 MS/s Sampling Rate ADC	12-bit, 11-channel, up to 2.8 MS/s Sampling Rate ADC
<b>UART</b>	2	2	4	4
<b>I/O</b>	I <sup>2</sup> C/SPI Master (6x) I <sup>2</sup> C/SPI Slave	I <sup>2</sup> C/SPI Master (6x)	I <sup>2</sup> C/SPI Master (7x) I <sup>2</sup> C/SPI Slave SDIO v3.0/eMMC (1x)	I <sup>2</sup> C/SPI Master (7x) I <sup>2</sup> C/SPI Slave USB 2.0 FS/HS SDIO v3.0/eMMC (1x)
<b>MSPI Master</b>	1/2/4/8-bit wide up to 48 MT/s (SDR) ISO7816 Master	1/2/4/8-bit wide (3x) up to 48 MT/s (SDR) ISO7816 Master	1/2/4/8-bit wide (2x) 1/2/4/8/16-bit wide up to 96 MT/s (SDR/DDR)	1/2/4/8-bit wide (2x) 1/2/4/8/16-bit wide (KXR pkg only) up to 96 MT/s (SDR/DDR)
<b>I<sup>2</sup>S</b>	I <sup>2</sup> S Slave for PDM Audio Pass-through	I <sup>2</sup> S Slave for PDM Audio Pass-through	I <sup>2</sup> S Master/Slave full-duplex	I <sup>2</sup> S Master/Slave (2x) full-duplex with ASRC (2x)
<b>Audio</b>	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (1x)	PDM stereo DMIC interface (4x) Low Power Stereo Audio ADC (1x)
<b>Display</b>	SPI 3-wire/4-wire	SPI 3-wire/4-wire Dual/QuadSPI	SPI 3-wire/4-wire Dual/QuadSPI	SPI 3-wire/4-wire Dual/QuadSPI MIPI DSI at 500 Mbps (1 lane) 4-layer Display Controller
<b>Graphics</b>	--	--	2D/2.5D GPU with anti-aliasing and dithering	2D/2.5D GPU with anti-aliasing, dithering, and vector graphics assist
<b>Security</b>	secureSPOT	secureSPOT w/ Secure Boot	secureSPOT 2.0 Secure Boot, Root of Trust	secureSPOT 2.0 Secure Boot, Root of Trust
<b>Connectivity</b>	Bluetooth <sup>®</sup> Low Energy 5	Bluetooth Low Energy 5	Bluetooth Low Energy 5.4	Bluetooth Low Energy 5.4
<b>RF Sensitivity</b>	-93 dBm (typical)	-93 dBm	-95 dBm	-95 dBm
<b>Tx Output Power</b>	Up to +3 dBm	Up to +3 dBm	Up to +6 dBm	Up to +6 dBm
<b>Packages</b>	<ul style="list-style-type: none"> <li>5 x 5 mm, BGA-81 w/ 50 GPIO</li> <li>3.25 x 3.37 x 0.51 mm, CSP-66 w/ 37 GPIO</li> <li>3.25 x 3.37 x 0.34 mm, CSP-66 w/ 37 GPIO (Thin)</li> <li>8 x 8 mm, QFN-64 w/ 38 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>5.3 x 4.3 x 0.8 mm, BGA-108 w/ 74 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>4.7 x 4.7 mm, BGA-131 w/ 75 GPIO</li> </ul>	<ul style="list-style-type: none"> <li>4.7 x 4.7 mm, BGA-131 w/ 81 GPIO</li> </ul>
<b>Ordering Information</b>	<ul style="list-style-type: none"> <li>AMA3B1KK-KBR-B0 (BGA)</li> <li>AMA3B1KK-KCR-B0 (CSP)</li> <li>AMA3B1KK-KCR-TB (Thin CSP)</li> <li>AMA3B1KK-KQR-B0 (QFN)</li> <li>AMA3BEVB (EVB)</li> </ul>	<ul style="list-style-type: none"> <li>AMA3B2KK-KBR (BGA)</li> <li>AMA3B2EVB (EVB)</li> </ul>	<ul style="list-style-type: none"> <li>AMA4B2KL-KXR (BGA)</li> <li>AMA4BLEVB (EVB)</li> </ul>	<ul style="list-style-type: none"> <li>AMA4B2KP-KXR (BGA)</li> <li>AMA4B2KP-KBR (BGA)</li> <li>AMAP4BPXEVB (EVB)</li> </ul>