



## PACKAGE QUALIFICATION REPORT

# Apollo3 Blue Plus AMA3B2KK-KBR

Ultra-Low Power Apollo SoC Family

A-SOC3BP-QRGA01EN v1.0



## Legal Information and Disclaimers

AMBIQ MICRO INTENDS FOR THE CONTENT CONTAINED IN THE DOCUMENT TO BE ACCURATE AND RELIABLE. THIS CONTENT MAY, HOWEVER, CONTAIN TECHNICAL INACCURACIES, TYPOGRAPHICAL ERRORS OR OTHER MISTAKES. AMBIQ MICRO MAY MAKE CORRECTIONS OR OTHER CHANGES TO THIS CONTENT AT ANY TIME. AMBIQ MICRO AND ITS SUPPLIERS RESERVE THE RIGHT TO MAKE CORRECTIONS, MODIFICATIONS, ENHANCEMENTS, IMPROVEMENTS AND OTHER CHANGES TO ITS PRODUCTS, PROGRAMS AND SERVICES AT ANY TIME OR TO DISCONTINUE ANY PRODUCTS, PROGRAMS, OR SERVICES WITHOUT NOTICE.

THE CONTENT IN THIS DOCUMENT IS PROVIDED "AS IS". AMBIQ MICRO AND ITS RESPECTIVE SUPPLIERS MAKE NO REPRESENTATIONS ABOUT THE SUITABILITY OF THIS CONTENT FOR ANY PURPOSE AND DISCLAIM ALL WARRANTIES AND CONDITIONS WITH REGARD TO THIS CONTENT, INCLUDING BUT NOT LIMITED TO, ALL IMPLIED WARRANTIES AND CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHT.

AMBIQ MICRO DOES NOT WARRANT OR REPRESENT THAT ANY LICENSE, EITHER EXPRESS OR IMPLIED, IS GRANTED UNDER ANY PATENT RIGHT, COPYRIGHT, MASK WORK RIGHT, OR OTHER INTELLECTUAL PROPERTY RIGHT OF AMBIQ MICRO COVERING OR RELATING TO THIS CONTENT OR ANY COMBINATION, MACHINE, OR PROCESS TO WHICH THIS CONTENT RELATE OR WITH WHICH THIS CONTENT MAY BE USED.

USE OF THE INFORMATION IN THIS DOCUMENT MAY REQUIRE A LICENSE FROM A THIRD PARTY UNDER THE PATENTS OR OTHER INTELLECTUAL PROPERTY OF THAT THIRD PARTY, OR A LICENSE FROM AMBIQ MICRO UNDER THE PATENTS OR OTHER INTELLECTUAL PROPERTY OF AMBIQ MICRO.

INFORMATION IN THIS DOCUMENT IS PROVIDED SOLELY TO ENABLE SYSTEM AND SOFTWARE IMPLEMENTERS TO USE AMBIQ MICRO PRODUCTS. THERE ARE NO EXPRESS OR IMPLIED COPYRIGHT LICENSES GRANTED HEREUNDER TO DESIGN OR FABRICATE ANY INTEGRATED CIRCUITS OR INTEGRATED CIRCUITS BASED ON THE INFORMATION IN THIS DOCUMENT. AMBIQ MICRO RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN. AMBIQ MICRO MAKES NO WARRANTY, REPRESENTATION OR GUARANTEE REGARDING THE SUITABILITY OF ITS PRODUCTS FOR ANY PARTICULAR PURPOSE, NOR DOES AMBIQ MICRO ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT, AND SPECIFICALLY DISCLAIMS ANY AND ALL LIABILITY, INCLUDING WITHOUT LIMITATION CONSEQUENTIAL OR INCIDENTAL DAMAGES. "TYPICAL" PARAMETERS WHICH MAY BE PROVIDED IN AMBIQ MICRO DATA SHEETS AND/OR SPECIFICATIONS CAN AND DO VARY IN DIFFERENT APPLICATIONS AND ACTUAL PERFORMANCE MAY VARY OVER TIME. ALL OPERATING PARAMETERS, INCLUDING "TYPICALS" MUST BE VALIDATED FOR EACH CUSTOMER APPLICATION BY CUSTOMER'S TECHNICAL EXPERTS. AMBIQ MICRO DOES NOT CONVEY ANY LICENSE UNDER NEITHER ITS PATENT RIGHTS NOR THE RIGHTS OF OTHERS. AMBIQ MICRO PRODUCTS ARE NOT DESIGNED, INTENDED, OR AUTHORIZED FOR USE AS COMPONENTS IN SYSTEMS INTENDED FOR SURGICAL IMPLANT INTO THE BODY, OR OTHER APPLICATIONS INTENDED TO SUPPORT OR SUSTAIN LIFE, OR FOR ANY OTHER APPLICATION IN WHICH THE FAILURE OF THE AMBIQ MICRO PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR. SHOULD BUYER PURCHASE OR USE AMBIQ MICRO PRODUCTS FOR ANY SUCH UNINTENDED OR UNAUTHORIZED APPLICATION, BUYER SHALL INDEMNIFY AND HOLD AMBIQ MICRO AND ITS OFFICERS, EMPLOYEES, SUBSIDIARIES, AFFILIATES, AND DISTRIBUTORS HARMLESS AGAINST ALL CLAIMS, COSTS, DAMAGES, AND EXPENSES, AND REASONABLE ATTORNEY FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PERSONAL INJURY OR DEATH ASSOCIATED WITH SUCH UNINTENDED OR UNAUTHORIZED USE, EVEN IF SUCH CLAIM ALLEGES THAT AMBIQ MICRO WAS NEGLIGENT REGARDING THE DESIGN OR MANUFACTURE OF THE PART.

## Revision History

Revision	Date	Description
1.0	July 12, 2023	Initial release

## Reference Documents

Document ID	Description

# Table of Contents

<b>1. Product Information .....</b>	<b>6</b>
1.1 Product Information .....	6
1.2 Qualification Results .....	6
<b>2. Pre-Conditioning (PC) .....</b>	<b>8</b>
<b>3. Temperature Cycle (TC) .....</b>	<b>9</b>
<b>4. High Temperature Storage Life (HTSL) .....</b>	<b>10</b>
<b>5. Unbiased Highly Accelerated Stress Test (UHAST) .....</b>	<b>11</b>
<b>6. Biased Highly Accelerated Stress Test (BHAST) .....</b>	<b>12</b>
<b>7. Mechanical Qual Tests .....</b>	<b>13</b>

## List of Tables

Table 1-1 Product Information .....	6
Table 1-2 Qualification Results .....	6
Table 2-1 Pre-Conditioning (PC) .....	8
Table 3-1 Temperature Cycle (TC) .....	9
Table 4-1 High Temperature Storage Life (HTSL) .....	10
Table 5-1 Unbiased Highly Accelerated Stress Test (UHAST) .....	11
Table 6-1 Biased Highly Accelerated Stress Test (BHAST) .....	12
Table 7-1 Mechanical Qual Tests .....	13

## SECTION

## 1

# Product Information

## 1.1 Product Information

Table 1-1: Product Information

<b>Product Type</b>	Ultra-Low Power SoC
<b>Part Number</b>	AMA3B2KK-KBR
<b>Process Technology</b>	40nm
<b>Foundry</b>	TSMC
<b>Package Type</b>	5.3 x 4.3 mm 104-pin BGA
<b>Assembly Site</b>	SPIL
<b>Date Code</b>	223901/224301/224302

## 1.2 Qualification Results

Table 1-2: Qualification Results

Test Item	Conditions	Lot No	Sample Size	Results
Pre-SAT	C-Scan + T-Scan	3	75	Pass (No delamination phenomenon on die surface)
Pre-Conditioning (PC)	JESD22-A113; MSL-3	3	75	Pass
Post-SAT	C-Scan + T-Scan	3	75	Pass (No delamination phenomenon on die surface)

Table 1-2: Qualification Results (*Continued*)

Test Item	Conditions	Lot No	Sample Size	Results
Temperature Cycle (TC)	JESD22-A104, TC condition B (-55°C – 125°C) for 700 Cycles	3	25	Pass 700 Cycles
High Temperature Storage Life (HTSL)	JESD22-A103; 150°C, 1000 hrs	3	25	Pass 1000 hrs
Unbiased Highly Accelerated Stress Test (UHAST)	EIA/JESD22-A118, +130°C / 85%R.H. Unbiased for 96 hrs	3	25	Pass 96 hrs
Biased Highly Accelerated Stress Test (BHAST)	EIA/JESD22-A110, +130°C / 85%R.H. Biased for 96 hrs	3	25	Pass 96 hrs
Mechanical Qual Tests	JEDEC/MIL-STD-883	3	25	Pass

## SECTION

## 2

## Pre-Conditioning (PC)

Table 2-1: Pre-Conditioning (PC)

<b>Test:</b>	Pre-Conditioning (PC)
<b>Purpose:</b>	The Preconditioning test stresses mechanical integrity by exposing a device to moisture soak conditions. Weakness and thermal expansion in die interconnections, die attach, and wire bonds are often detected with this acceleration test.
<b>Conditions:</b>	JESD22-A113; MSL-3 <ul style="list-style-type: none"> <li>▪ Electrical Full Functional Test</li> <li>▪ External Visual Examination</li> <li>▪ Dry Bake at 125°C for 24 hrs</li> <li>▪ Soak Devices to appropriate Moisture Level e.g., MSL 3 (30°C/60%RH, 192hrs)</li> <li>▪ IR Reflow with JEDEC Standard Profile, 3 times</li> <li>▪ External Visual Examination</li> </ul>
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	75 ea/lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test.
<b>Results</b>	Pass



## SECTION

## 3

## Temperature Cycle (TC)

Table 3-1: Temperature Cycle (TC)

<b>Test:</b>	Temperature Cycle Test (TC)
<b>Purpose:</b>	Temperature Cycle Testing (TC), or simply temperature cycling or temp cycling, determines the ability of parts to resist extremely low and extremely high temperatures, as well as their ability to withstand cyclical exposures to these temperature extremes. A mechanical failure resulting from cyclical thermomechanical loading is known as a fatigue failure, so temperature cycling primarily accelerates fatigue failures.
<b>Conditions:</b>	JESD22-A104, -55°C to 125°C Air to Air (Pre-Conditioned units)
<b>Duration:</b>	700 Cycles
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	25 ea/ lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test.
<b>Results</b>	Pass 700 Cycles

## SECTION

## 4

## High Temperature Storage Life (HTSL)

Table 4-1: High Temperature Storage Life (HTSL)

<b>Test:</b>	High Temperature Storage Life (HTSL)
<b>Purpose:</b>	The test is typically used to determine the effects of time and temperature, under storage conditions, for thermally activated failure mechanisms and time-to-failure distributions of solid state electronic devices, including nonvolatile memory devices (data retention failure mechanisms). Thermally activated failure mechanisms are modeled using the Arrhenius Equation for acceleration. During the test, accelerated stress temperatures are used without electrical conditions applied.
<b>Conditions:</b>	JESD22-A103; +150°C for 1000 continuous hours
<b>Duration:</b>	1000 hrs
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	25 ea/ lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test.
<b>Results</b>	Pass 1000 hrs

## SECTION

## 5

## Unbiased Highly Accelerated Stress Test (UHAST)

Table 5-1: Unbiased Highly Accelerated Stress Test (UHAST)

<b>Test:</b>	Unbiased Highly Accelerated Stress Test (UHAST)
<b>Purpose:</b>	The test is performed for the purpose of evaluating the reliability of non-hermetic packaged solid-state devices in humid environments. It is a highly accelerated test that employs temperature and humidity under non-condensing conditions to accelerate the penetration of moisture through the external protective material (encapsulant or seal) or along the interface between the external protective material and the metallic conductors that pass through it.
<b>Conditions:</b>	EIA/JESD22-A118, +130°C / 85%R.H. Unbiased (Pre-Conditioned units)
<b>Duration:</b>	96 hrs
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	25 ea/ lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test
<b>Results</b>	Pass 96 hrs

## SECTION

## 6

## Biased Highly Accelerated Stress Test (BHAST)

Table 6-1: Biased Highly Accelerated Stress Test (BHAST)

<b>Test:</b>	Biased Highly Accelerated Stress Test (BHAST)
<b>Purpose:</b>	The test is performed for the purpose of evaluating the reliability of non-hermetic packaged solid-state devices in humid environments. It employs severe conditions of temperature, humidity, and bias which accelerate the penetration of moisture through the external protective material (encapsulant or seal) or along the interface between the external protective material and the metallic conductors which pass through it.
<b>Conditions:</b>	EIA/JESD22-A110, +130°C / 85%R.H. Biased (Pre-Conditioned units)
<b>Duration:</b>	96 hrs
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	25 ea/ lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test
<b>Results</b>	Pass 96 hrs

## SECTION

## 7

# Mechanical Qual Tests

Table 7-1: Mechanical Qual Tests

<b>Test:</b>	Mechanical Qual Tests
<b>Purpose:</b>	The test is to measure mechanical strength such as bond/ball strengths, evaluate bond/ball strength distributions, or determine compliance with specified bond/ball strength requirements of the applicable document.
<b>Conditions:</b>	JEDEC/MIL-STD-883
<b>Duration:</b>	96 hrs
<b>Lot No:</b>	3 lot
<b>Sample Size:</b>	25 ea/ lot
<b>Pass/Fail Criteria:</b>	Zero failure in function test
<b>Results</b>	Pass



© 2023 Ambiq Micro, Inc. All rights reserved.

6500 River Place Boulevard, Building 7, Suite 200, Austin, TX 78730

[www.ambiq.com](http://www.ambiq.com)

[sales@ambiq.com](mailto:sales@ambiq.com)

+1 (512) 879-2850

A-SOC3BP-QRGA01EN v1.0

July 2023